

`/usr/share/doc/alliance-doc-5.0/tutorial/place_and_route/src`

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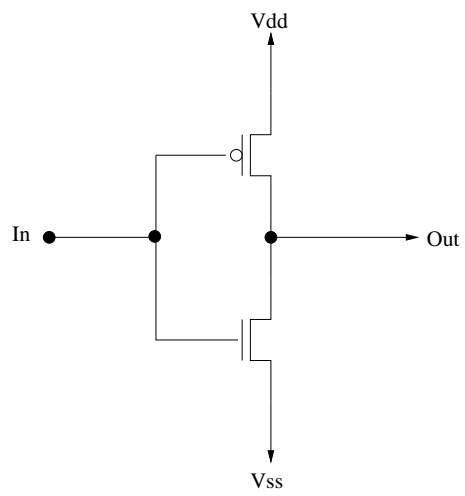
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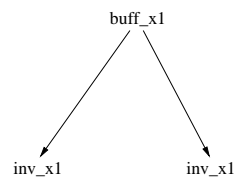
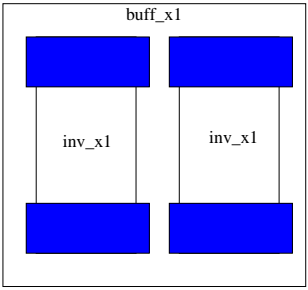
```
> cougar -t file1 file2
```

```
> MBK_IN_PH = ap
> export MBK_IN_PH
> MBK_OUT_LO = spi
> export MBK_OUT_LO
```

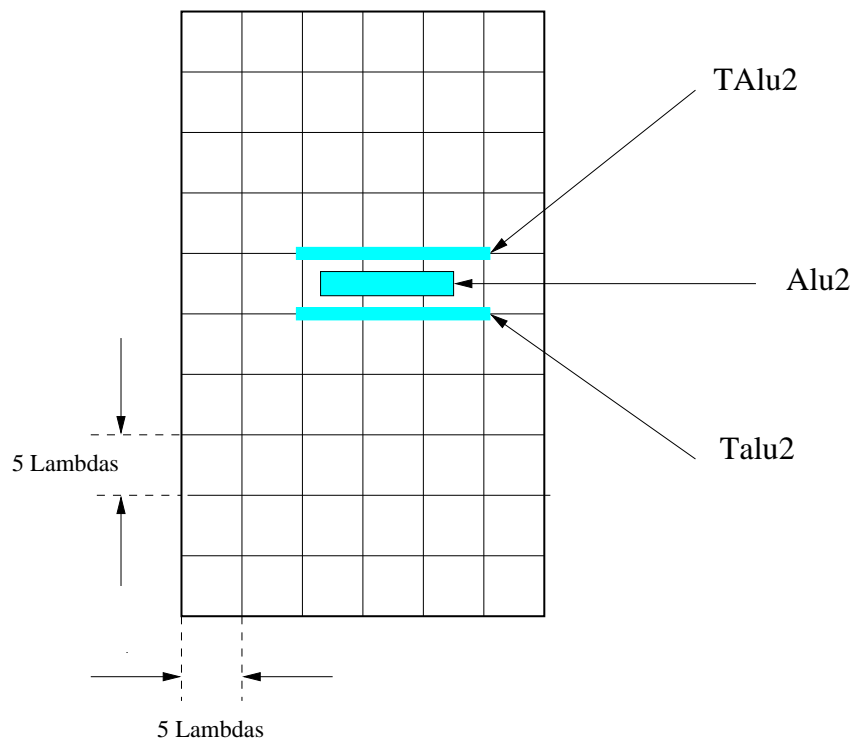
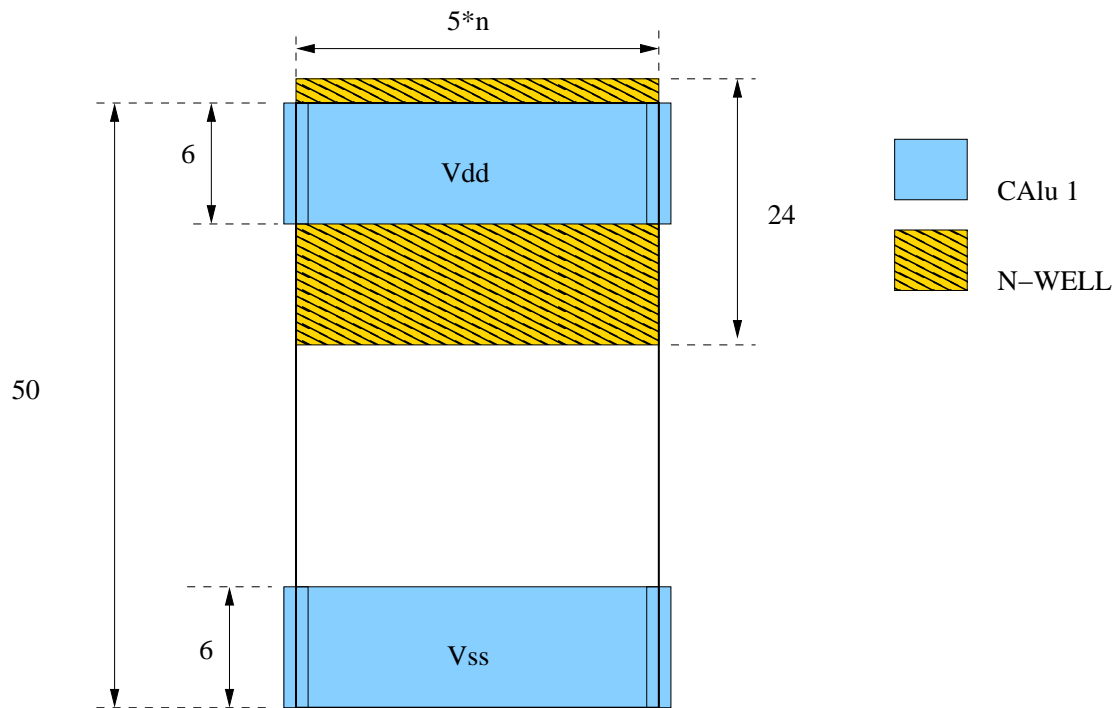
```
> cougar -t circuit circuit
```

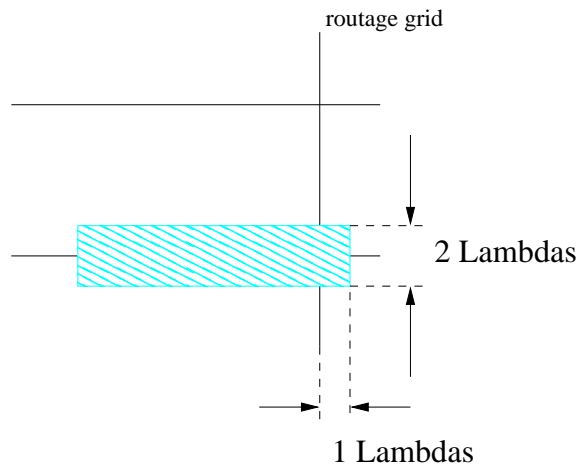
```
> xsch -I spi -l circuit
```



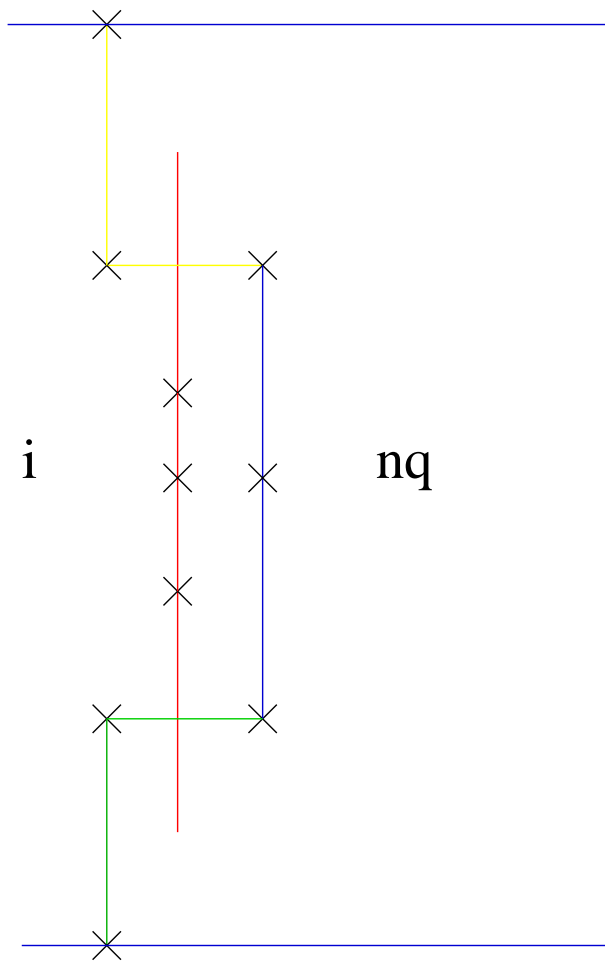


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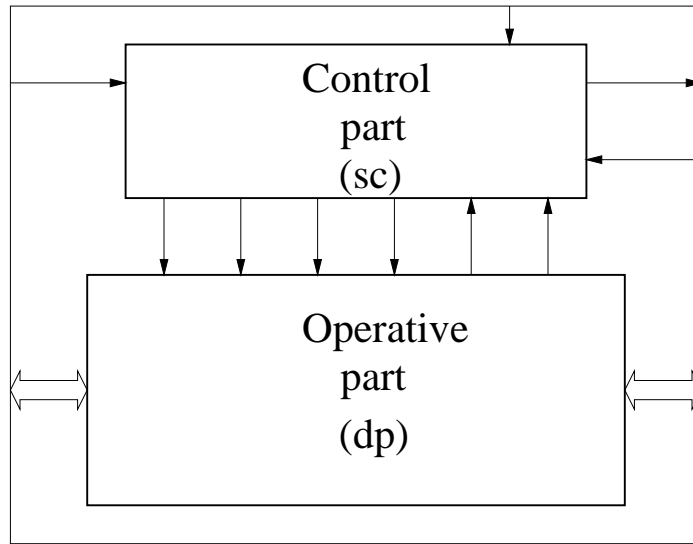
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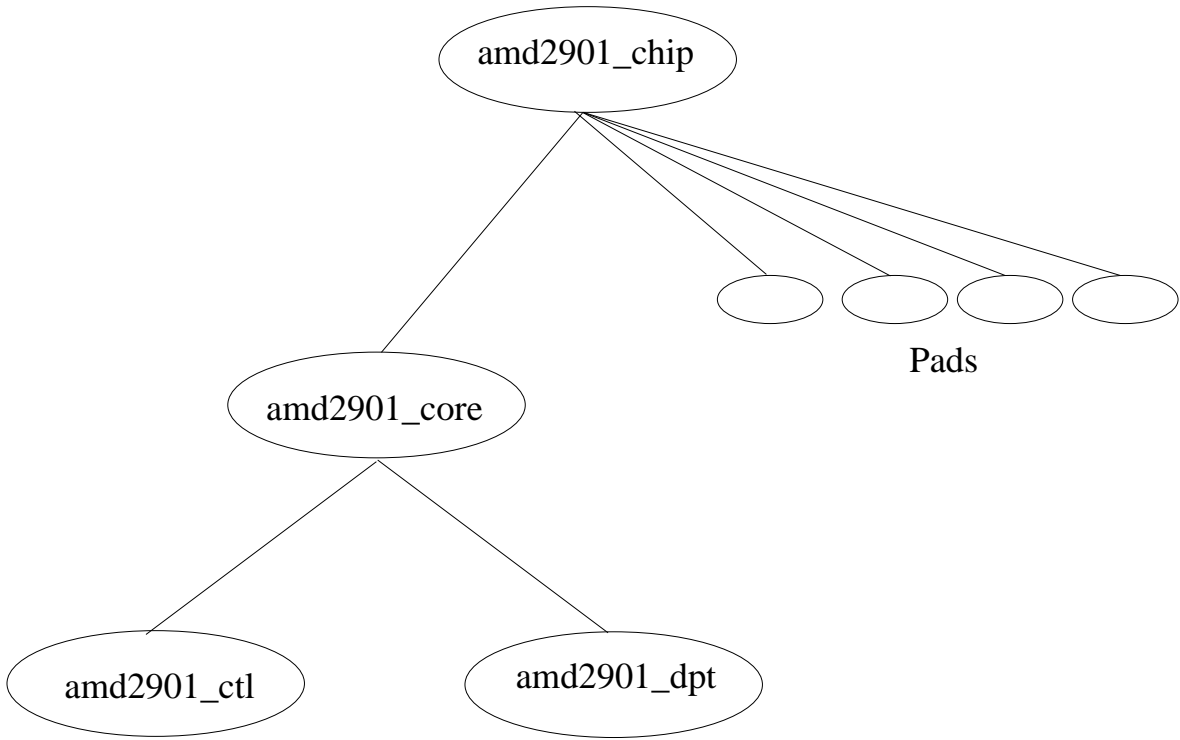
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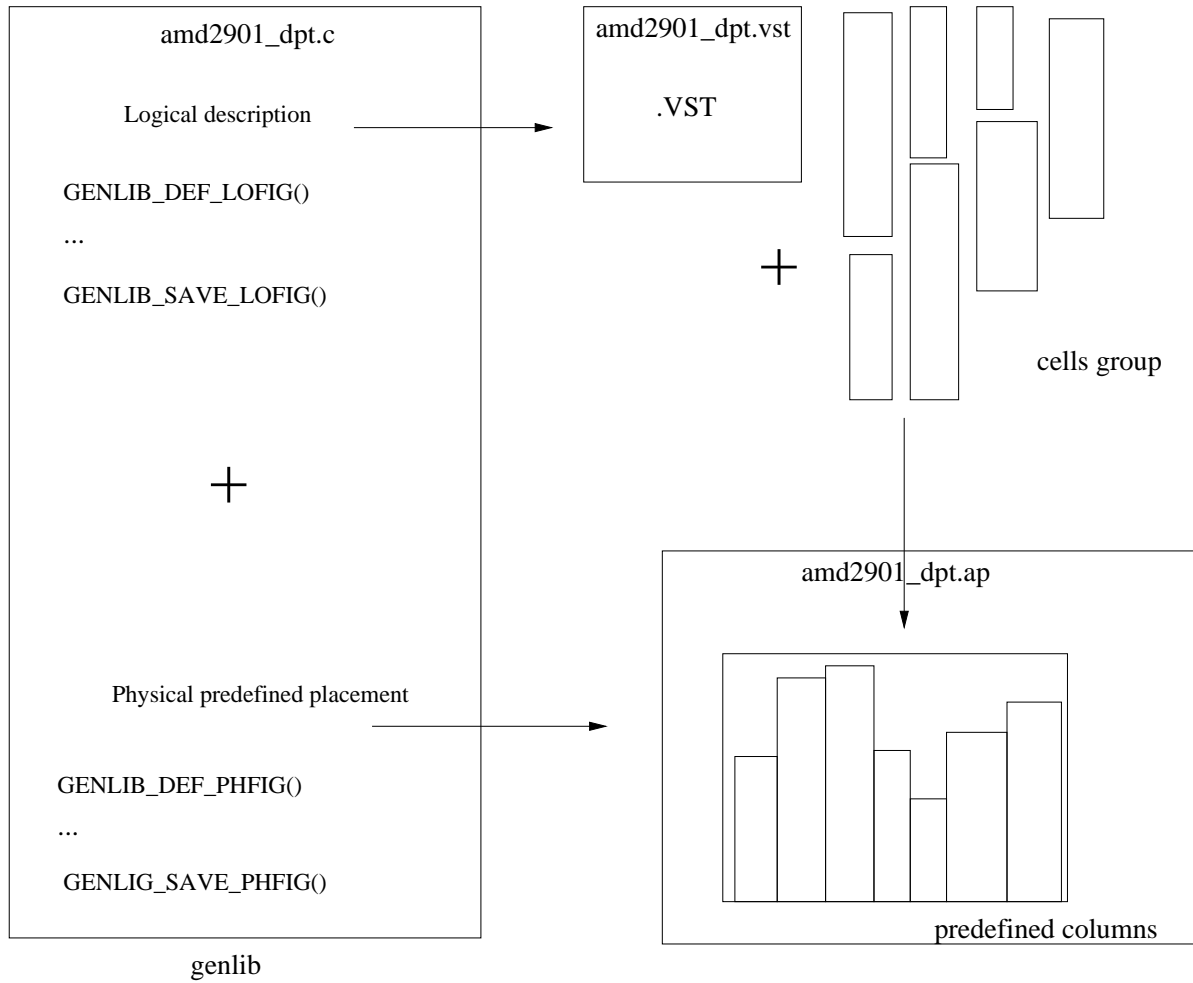
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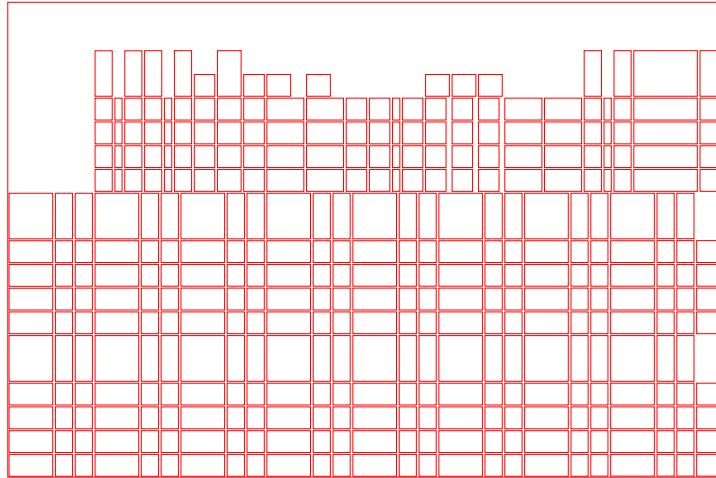


```
> VH_MAXERR = 10
> export VH_MAXERR
> MBK_WORK_LIB = .
> export MBK_WORK_LIB
> MBK_CATA_LIB = $ALLIANCE_TOP/cells/sxlib
> export MBK_CATA_LIB
> MBK_CATA_LIB = $MBK_CATA_LIB:$ALLIANCE_TOP/cells/dp_sxlib
> export MBK_CATA_LIB
> MBK_CATA_LIB = $MBK_CATA_LIB:$ALLIANCE_TOP/cells/padlib
> export MBK_CATA_LIB
> MBK_CATA_LIB $MBK_CATA_LIB:.
> export MBK_CATA_LIB
> MBK_CATAL_NAME = CATAL
> export MBK_CATAL_NAME
> MBK_IN_LO = vst
> export MBK_IN_LO
> MBK_OUT_LO = vst
> export MBK_OUT_LO
> MBK_IN_PH = ap
> export MBK_IN_PH
> MBK_OUT_PH = ap
> export MBK_OUT_PH
```

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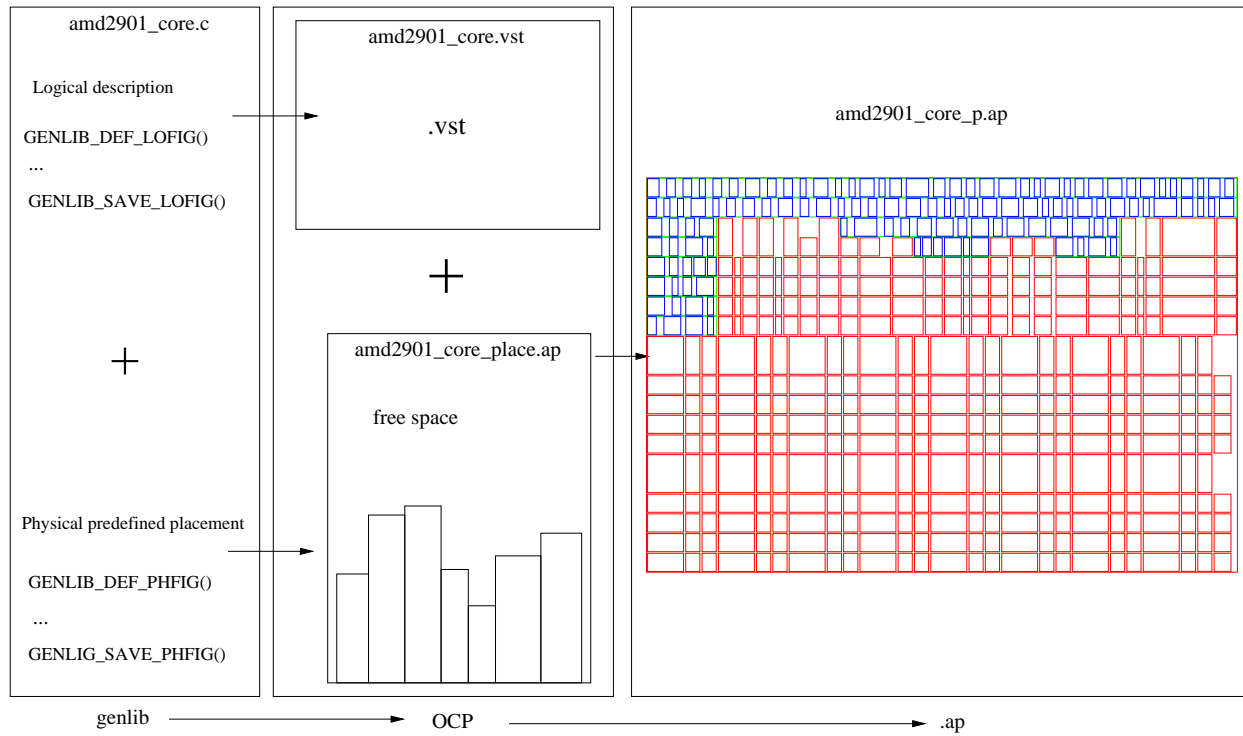
```
>genlib amd2901_dpt
```





```
> genlib amd2901_core
```

```
> ocp -partial amd2901_core -ioc amd2901_core amd2901_core amd2901_core_p
```



```
> nero -v -3 -p amd2901_core_p amd2901_core amd2901_core
```

```
> east ( pi1 pi0 )
> west ( pck pi4 )
> north ( pvdd pvss )
> south ( pvdde pvsse )
```

```
> ring amd2901_chip amd2901_chip
```

```
> druc amd2901_chip
```

```
> MBK_OUT_LO = al  
> export MBK_OUT_LO
```

```
> cougar -f amd2901_chip
```

```
> lvx vst al amd2901_chip amd2901_chip -f
```

```
> MBK_OUT_LO = vst  
> export MBK_OUT_LO
```

```
> cougar -t amd2901_chip amd2901_chip
```

```
> make view_ctl_logic
```

```
> make view_dpt_physic
```

```
> make view_chip_physic
```

```
> make view_chip_simulation
```

